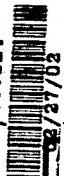


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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10086214	FILING DATE 02/27/2002	CLASS 714	SUBCLASS 72.6	GAU 2184	EXAMINER <i>Deady</i>
**APPLICANTS: Wang Laung-Terng; Chang Ming-Tung; Lin Shyh-Horng; Chao Hao-Jan; Lee Jaehee; Wang Hsin-Po; Wen Xiaoqing; Hsu Po-Ching; Kao Shih-Chia; Lin Meng-Chyi; Tsai Sen-Wei; Hsu Chi-Chan;					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/272,064 03/01/2001					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		3175-Z			
Verified and Acknowledged Examiners's initials					
TITLE : Method and apparatus for diagnosing failures in an integrated circuit using design-for-debug (DFD) techniques					
U.S. DEPT. OF COMM. / PAT. & TM. PTO-436 (Rev. 12-94)					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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